



\$AF
2185

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Title: MULTIMEDIA INTERFACE HAVING A PROCESSOR AND RECONFIGURABLE LOGIC (As Previously Amended)

Serial No.: 09/929,515

Filed: 08/14/2001

Applicant(s): MUTHUKUMARASWAMY, et al.

Examiner: Cao, C.

Art Unit: 2185

AMENDMENT AFTER FINAL

IN THE CLAIMS

Please amend the claims, as follows.

Cancel **claims 48 and 54.**

RECEIVED

MAY 27 2004

Technology Center 2100

23. (Canceled) ~~Multimedia interface, comprising:~~
~~— an integrated circuit (IC) chip;~~
~~— a block of reconfigurable logic incorporated on the IC chip; and~~
~~— a block of media processor with a virtual instruction set capable of implementing a~~
~~variety of multimedia algorithms incorporated on the IC chip separately from the reconfigurable~~
~~logic block.~~

24. (Previously Amended) The multimedia interface, according to claim 59, further comprising audio and/or video CODEC incorporated on the IC chip.

25. (Previously Amended) The multimedia interface, according to claim 59, further comprising a phase locked loop (PLL) circuitry incorporated on the IC chip.

26. (Previously Amended) The multimedia interface according to claim 59, further comprising a programmable, fast serial interface core to interface to a serial interface standard incorporated on the IC chip.

27. (Previously Amended) The multimedia interface according to claim 59, further comprising a programmable CPU interface core incorporated on the IC chip.